

PATENT ABSTRACTS OF JAPAN

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(21)Application number : 05-247372

(71)Applicant : KYODO PRINTING CO LTD

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(54) MICROCOMPUTER FOR IC CARD

(57)Abstract:

PURPOSE: To obtain a microcomputer for an IC card capable of securing the secrecy of a program.

CONSTITUTION: An one-chip MPU including a RAM, a mask ROM and an EEPROM has a read limiting circuit 46 also. Since the reading of a program in a specific area of the mask ROM is inhibited by the circuit 46, the secrecy of program constitution is secured and data recorded in the mask ROM are prevented from being decoded. The circuit 46 has a read limitation flag generating circuit 15 for selectively setting up the execution of inhibition, a judging circuit 1 for judging the existence of a program stored in the EEPROM, a judging circuit 2 for judging the existence of a read instruction and a judging circuit 3 for judging the existence of an access to a user reference enabled area in the mask ROM, and when all conditions are satisfied, a read limiting signal is outputted from an AND circuit 18.

